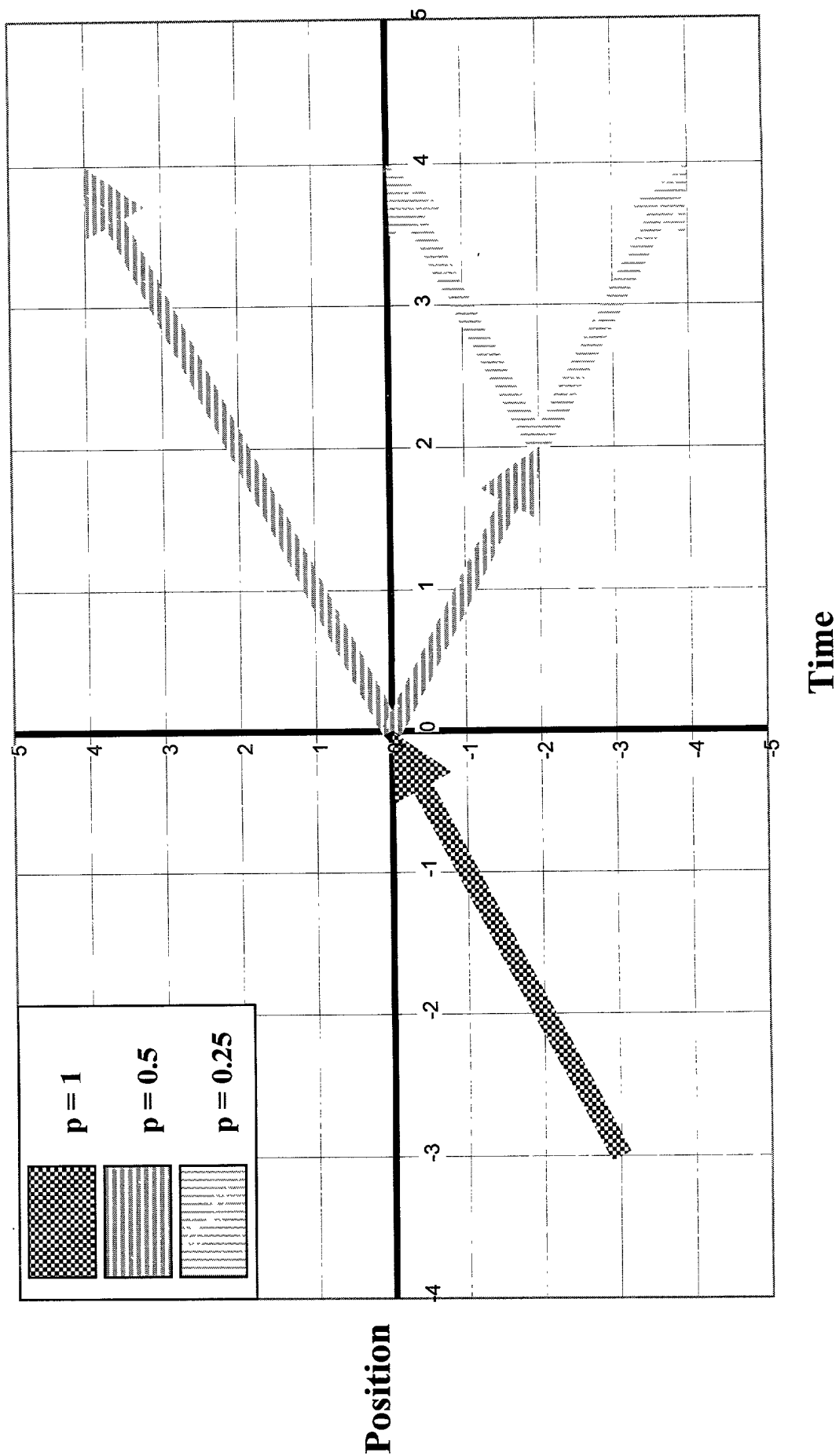
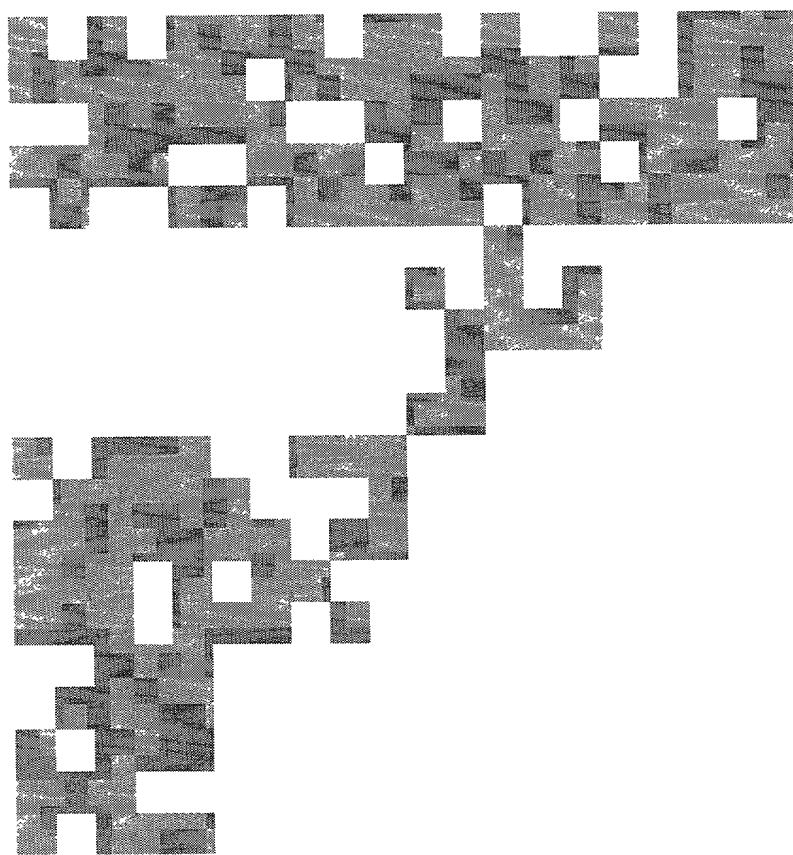
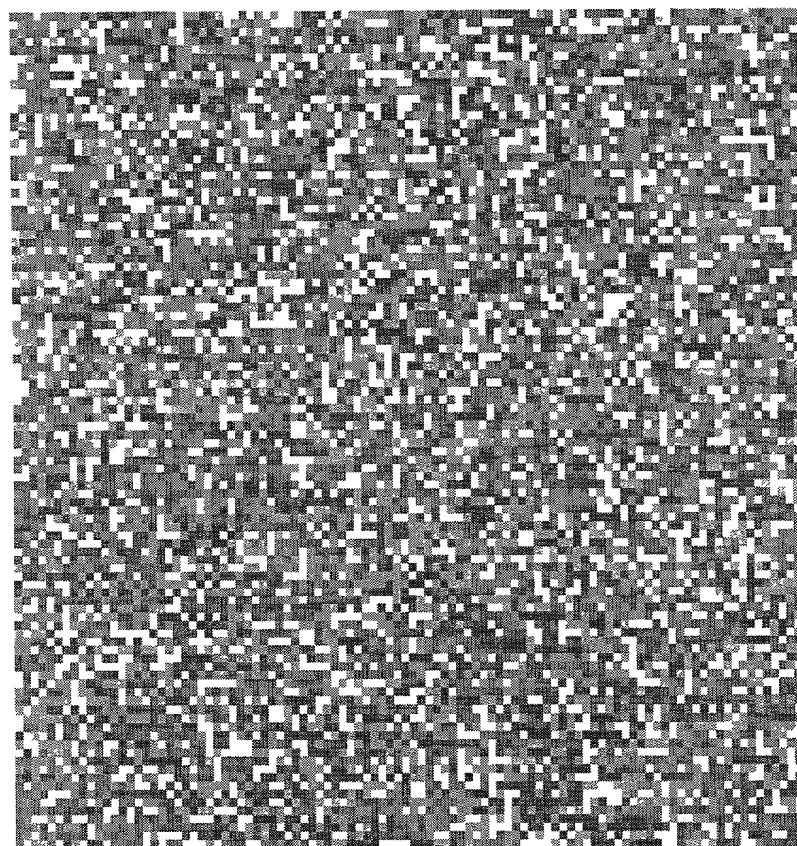


# Figure 1



[illegible]

# Figure 2



## Figure 3



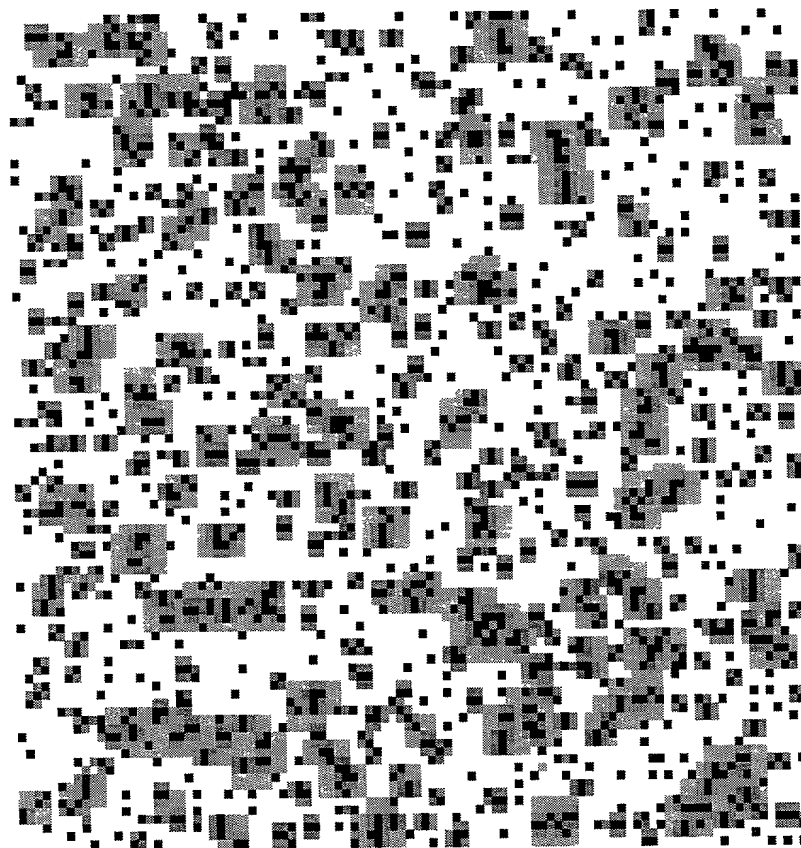
# Figure 4



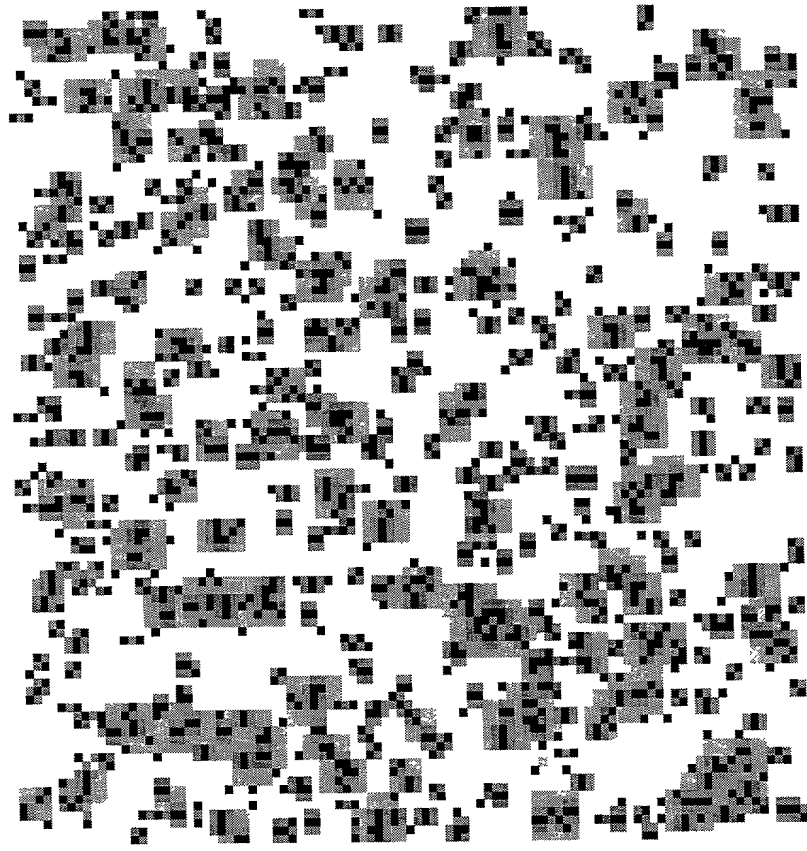
## Figure 5



# Figure 6



## Figure 7



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## Figure 8

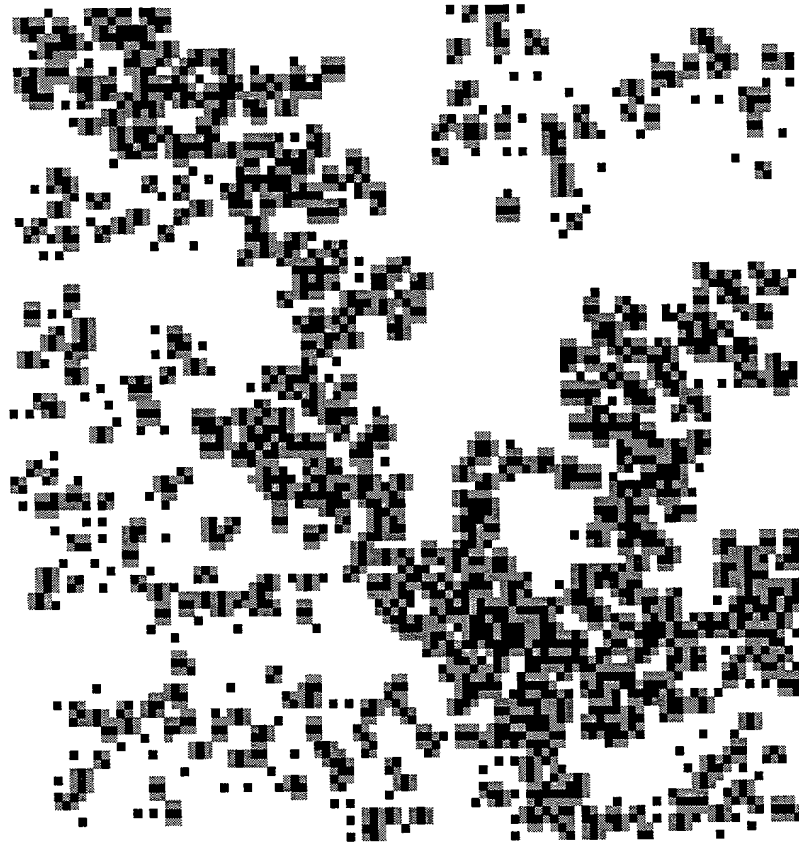




## Figure 9

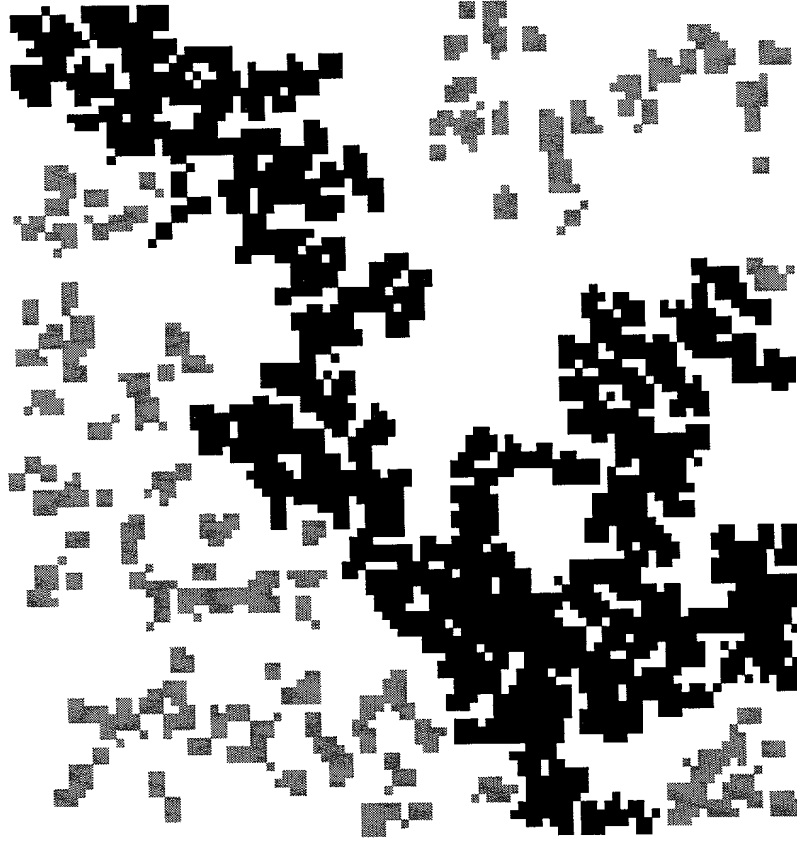


## Figure 10



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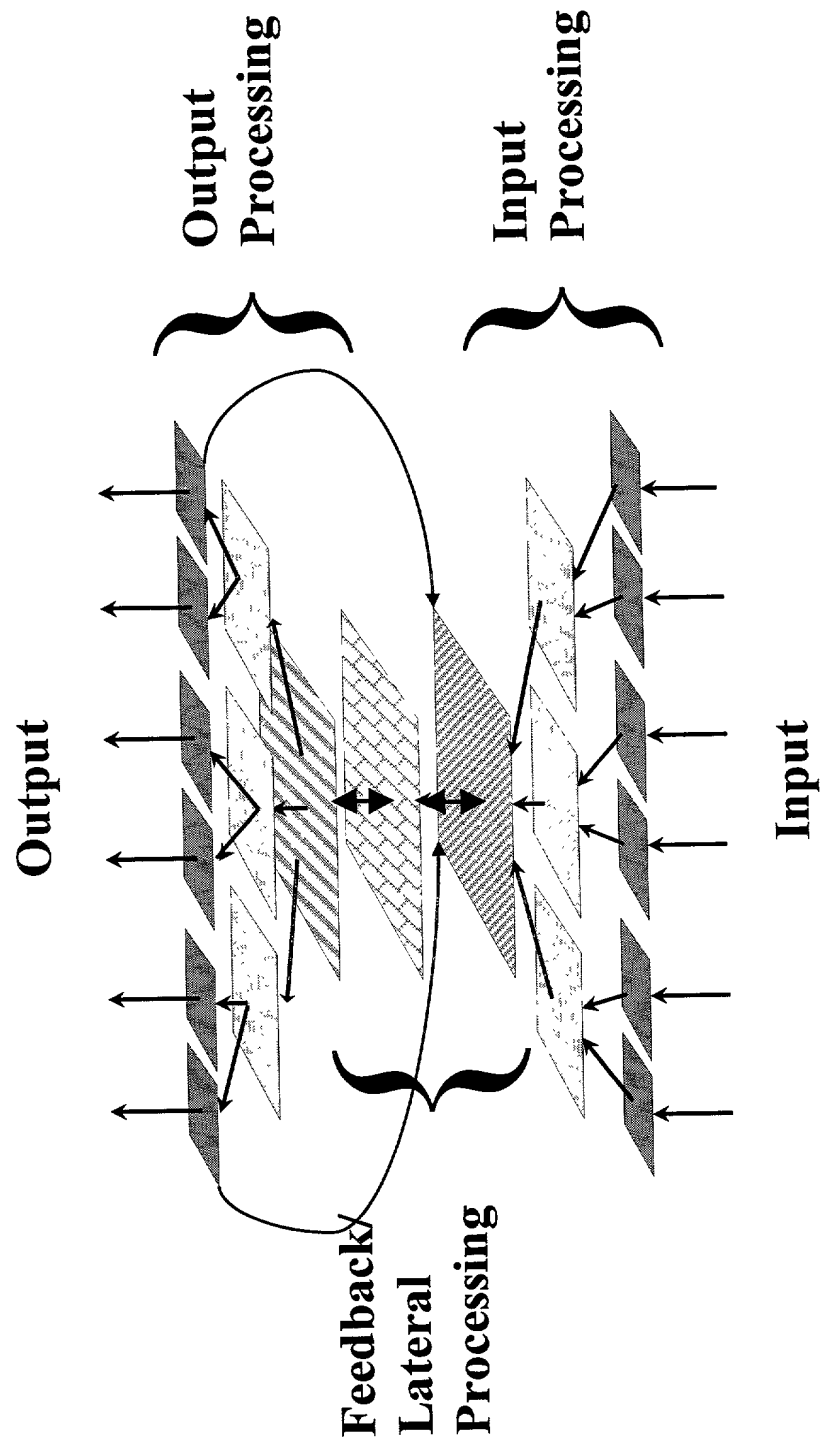
## Figure 11



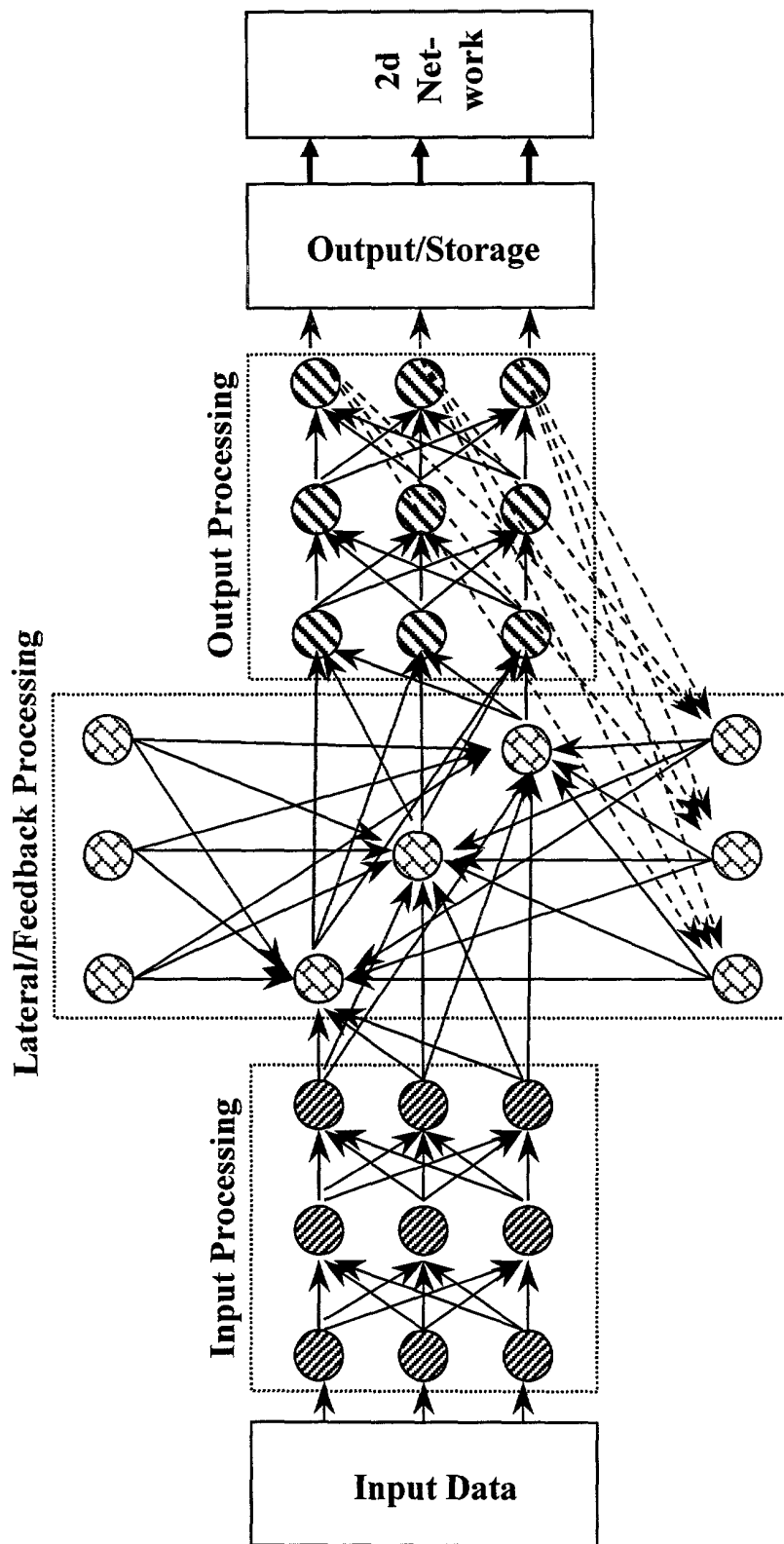
## Figure 12



# Figure 13

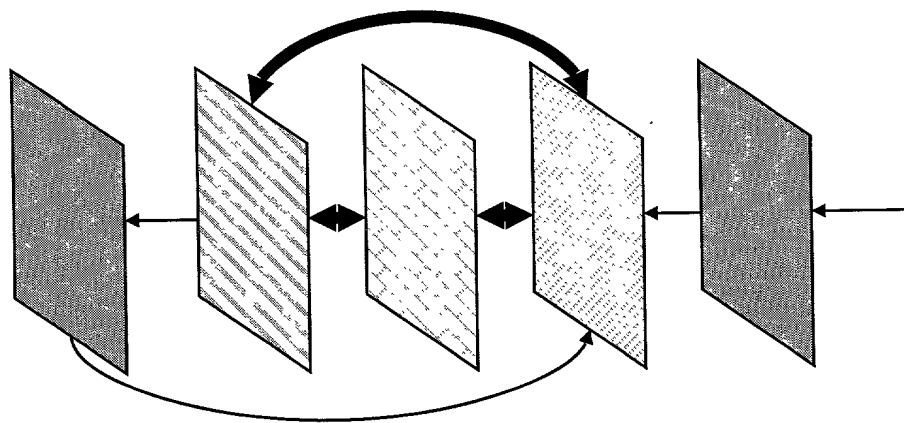


# Figure 14

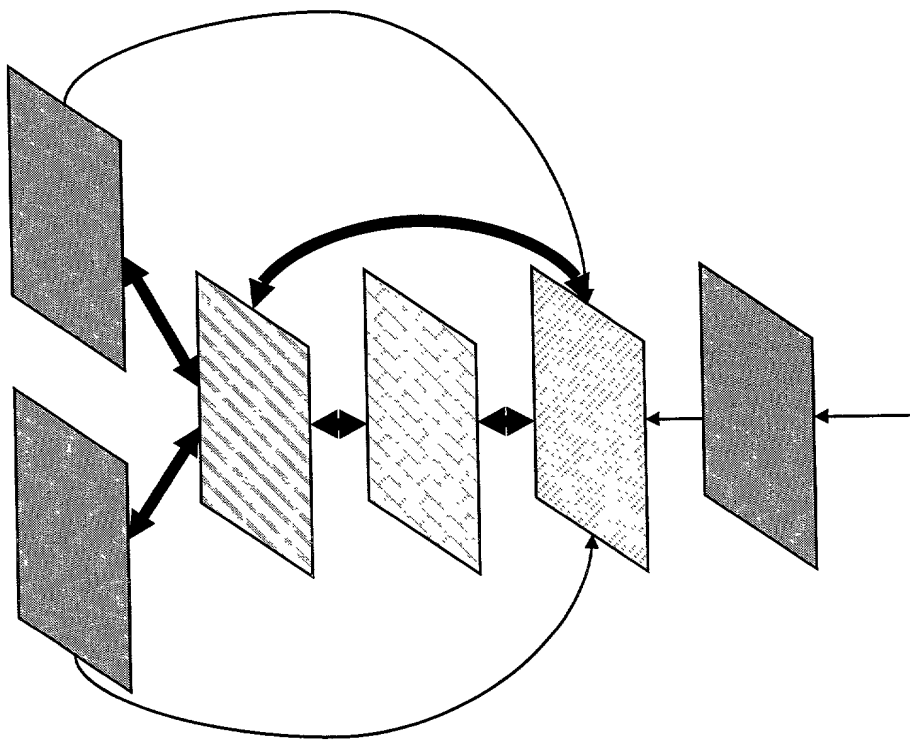


# Figure 15

15A. 3-Layer Processing;  
Feedforward Output

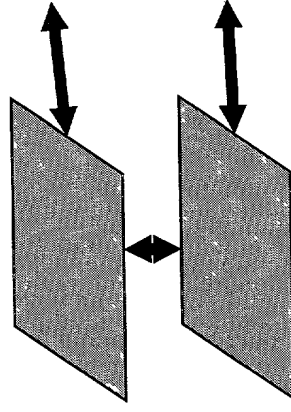


15B. 3-Layer Processing;  
Bidirectional Output



# Figure 16

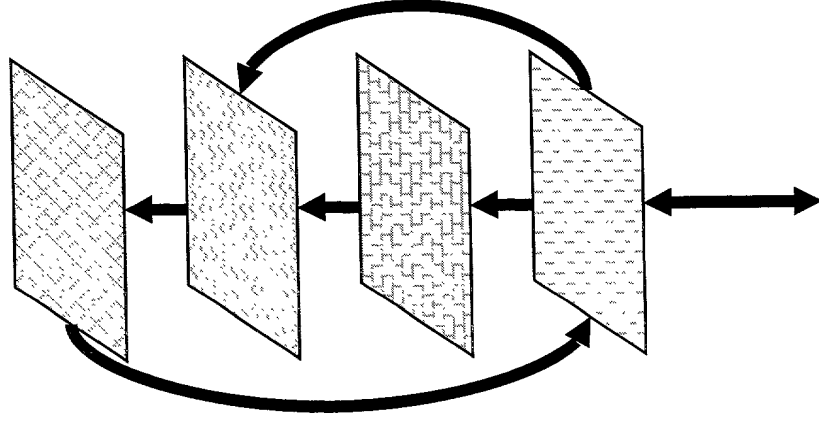
Attention Module



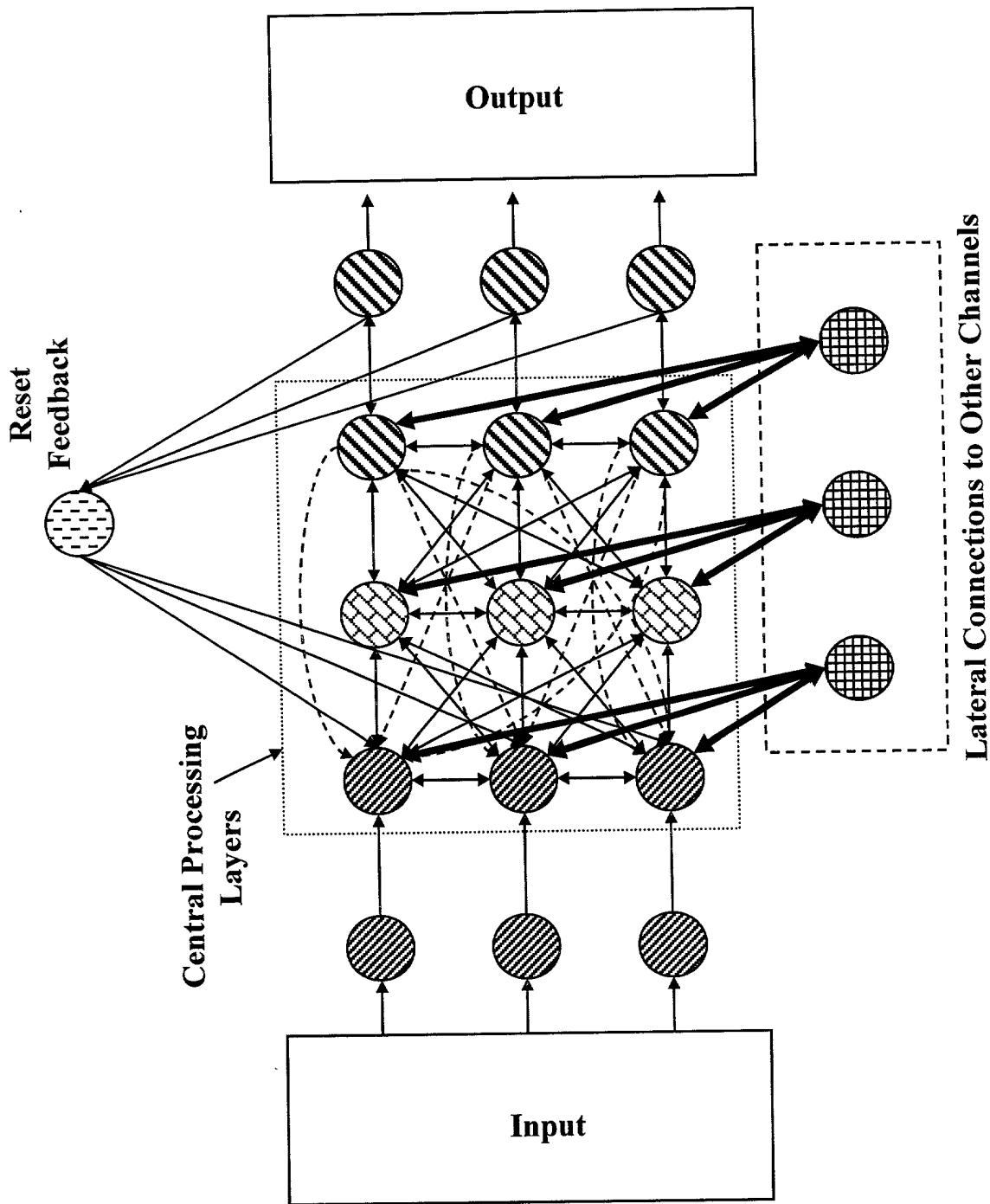


# Figure 17

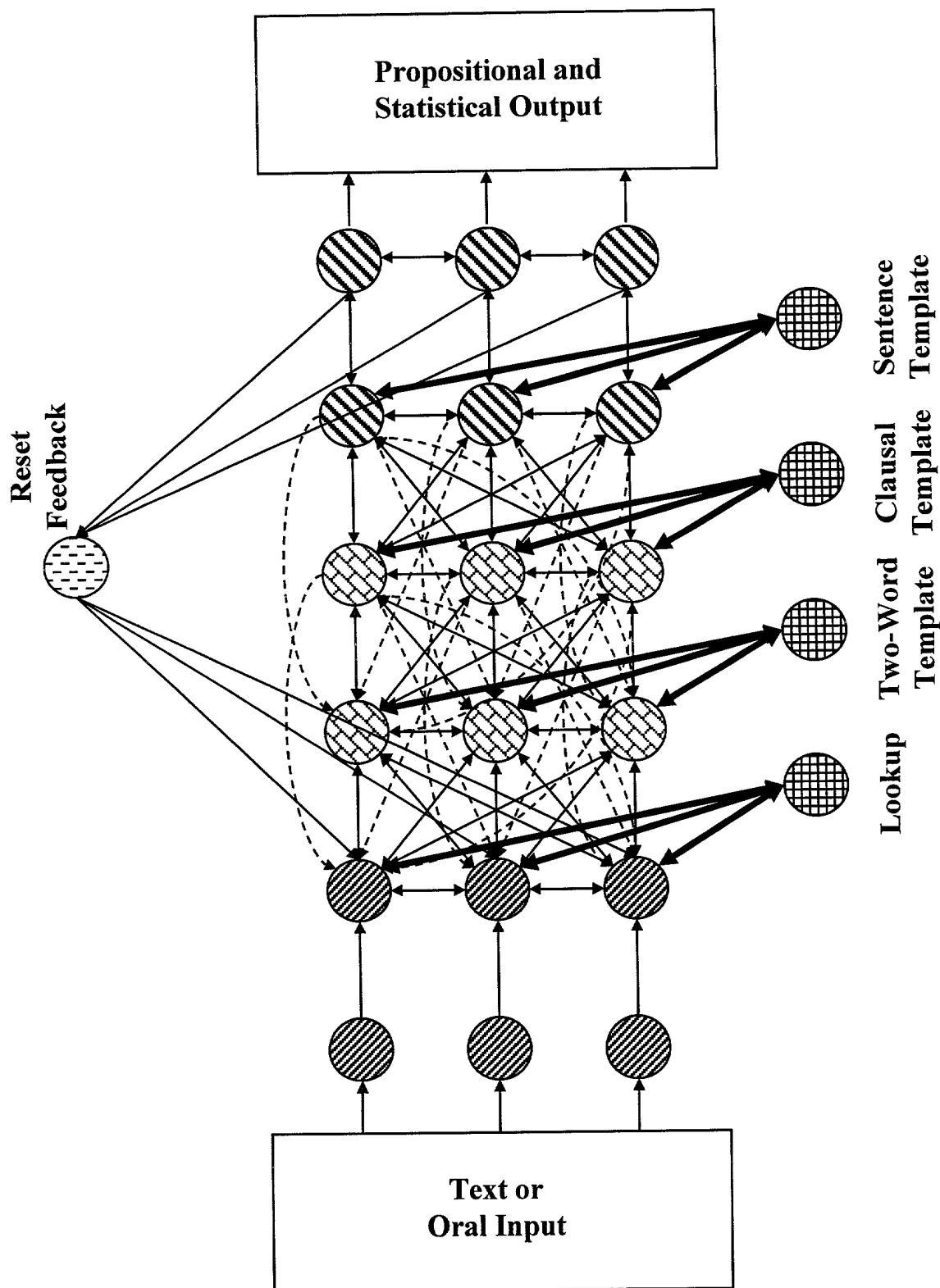
Long-Term Memory Module



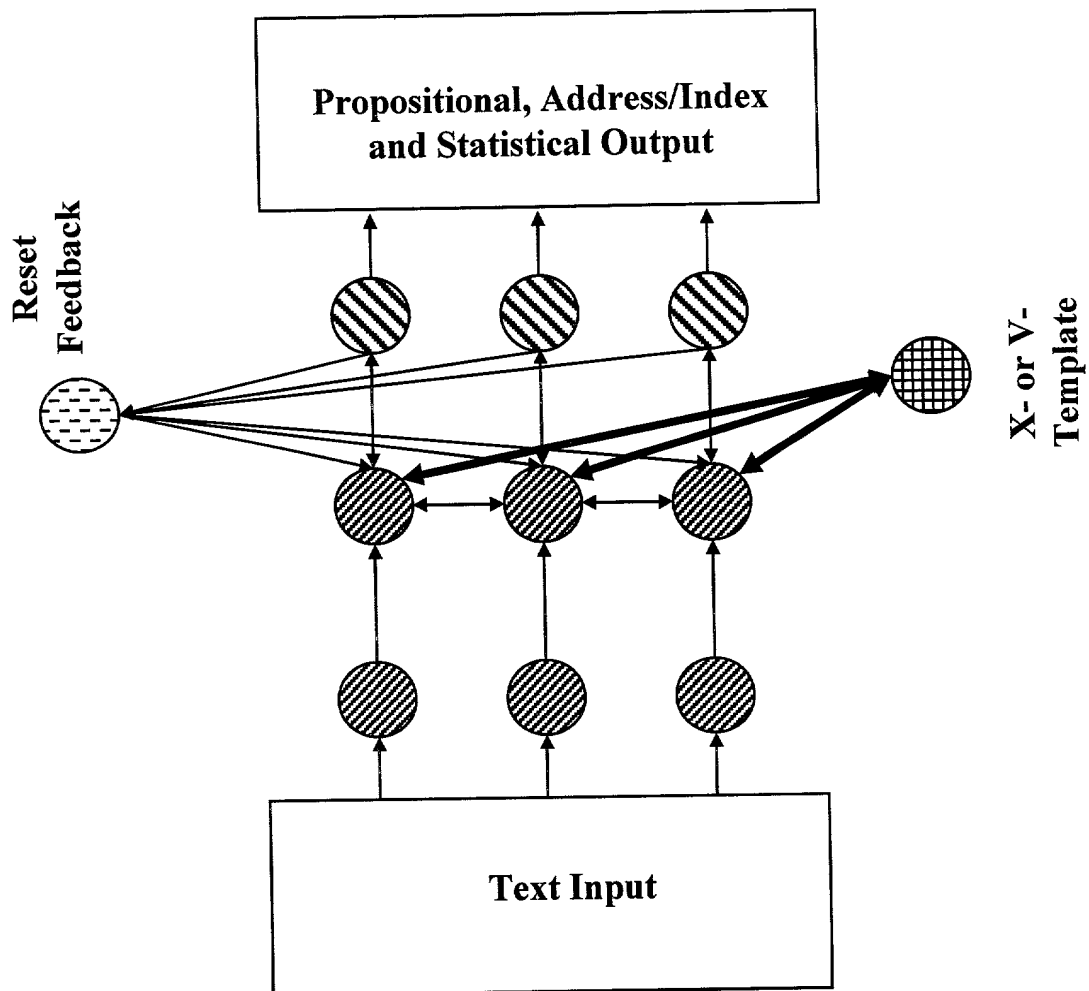
# Figure 18



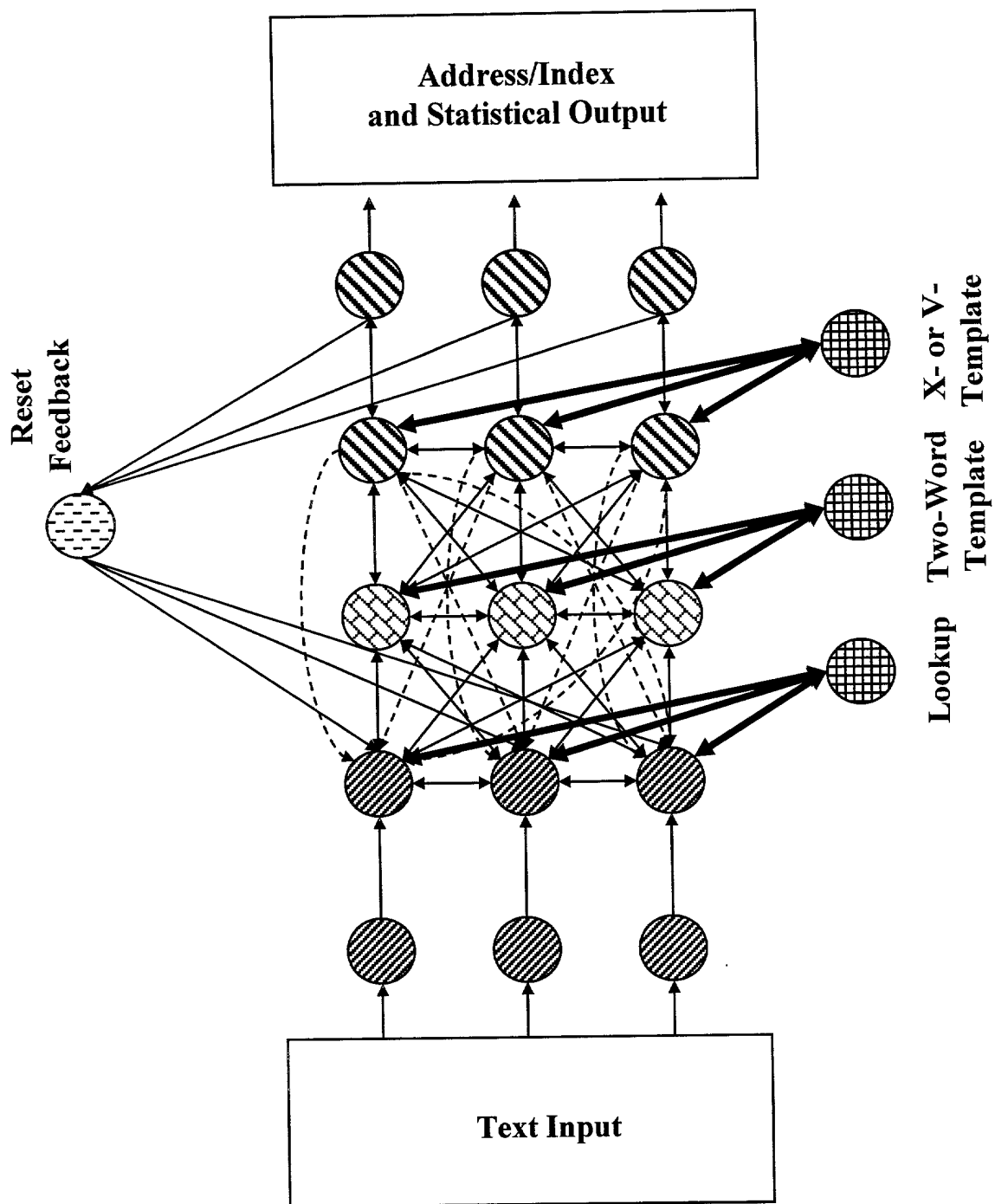
# Figure 19



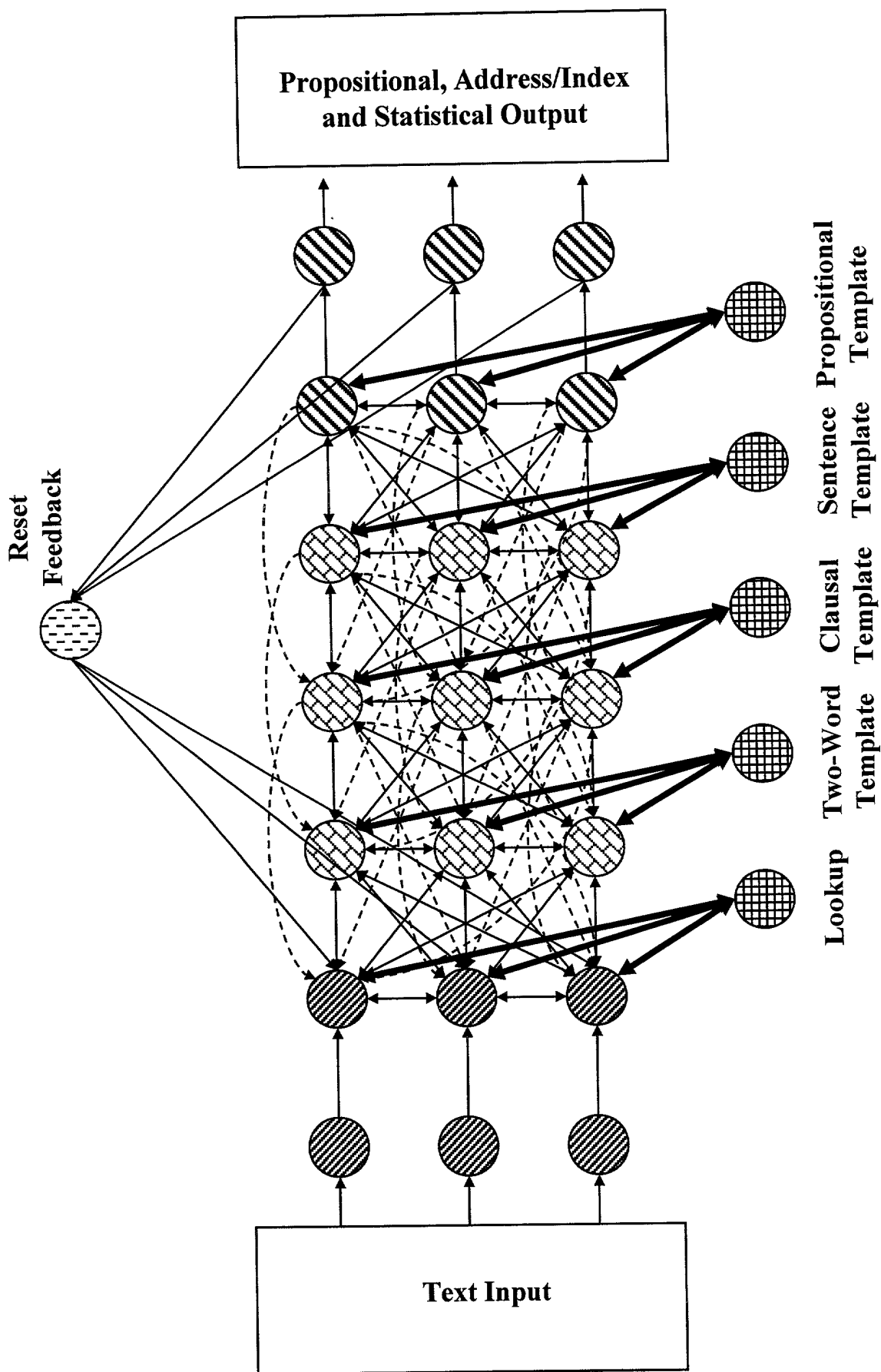
# Figure 20



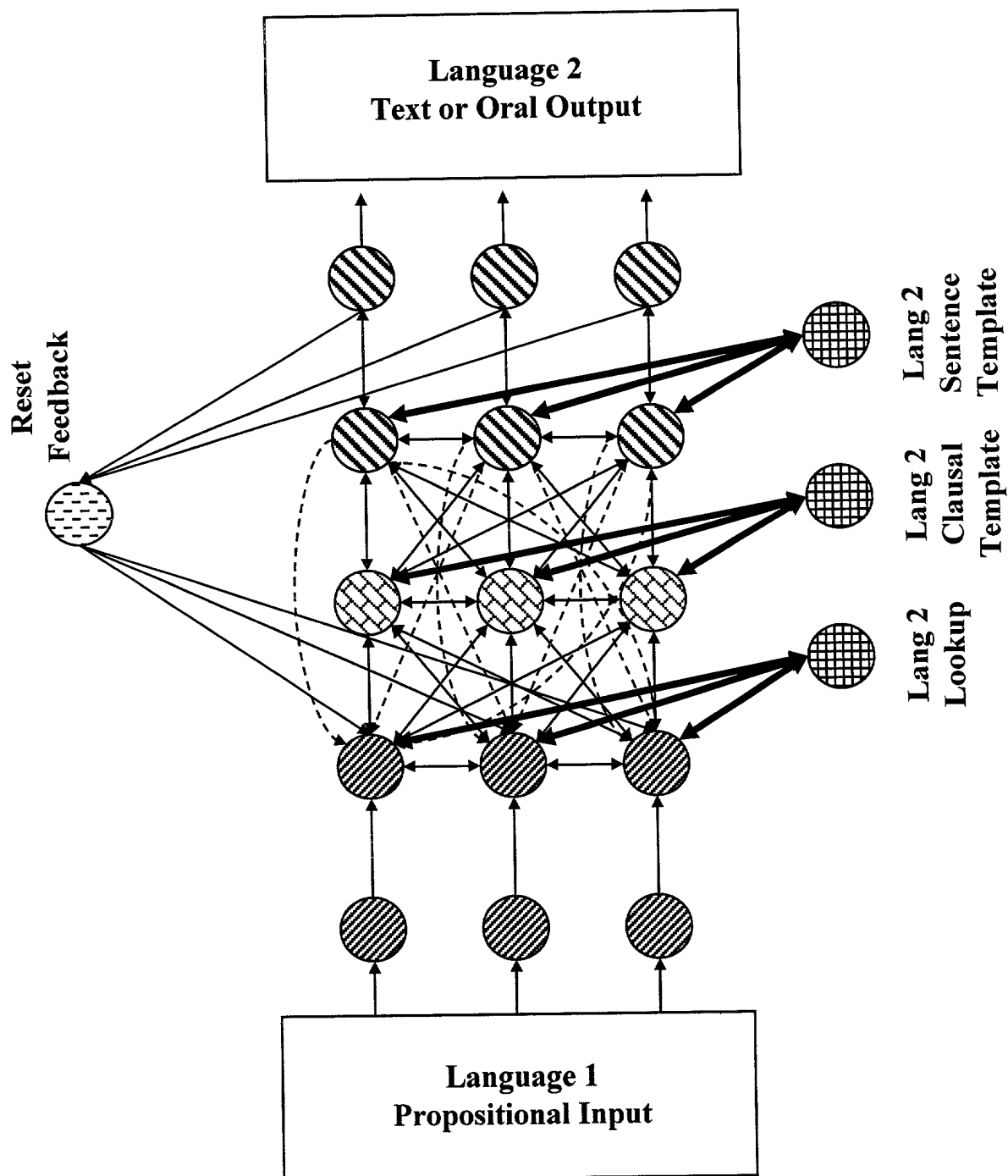
# Figure 21



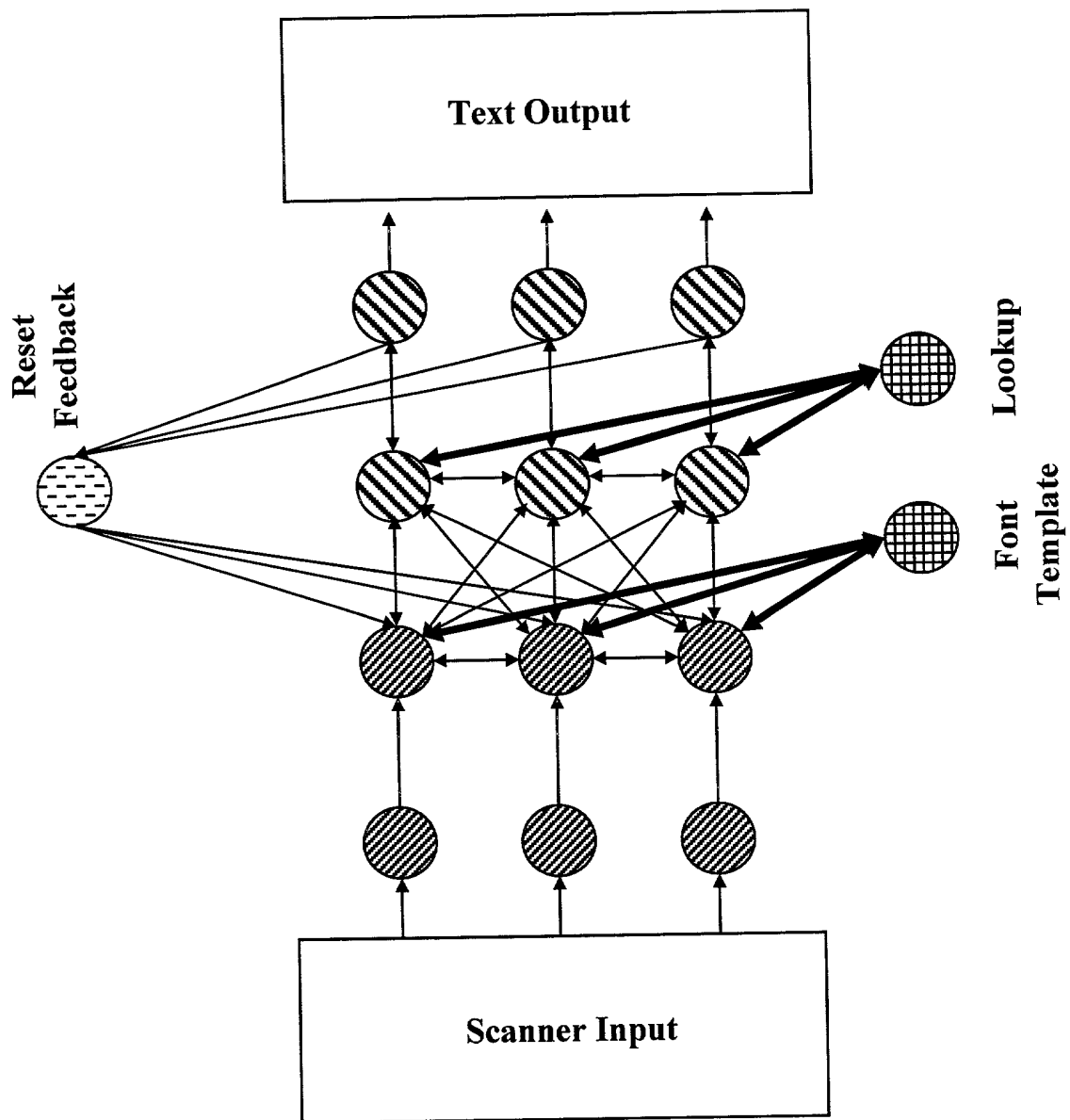
# Figure 22



# Figure 23

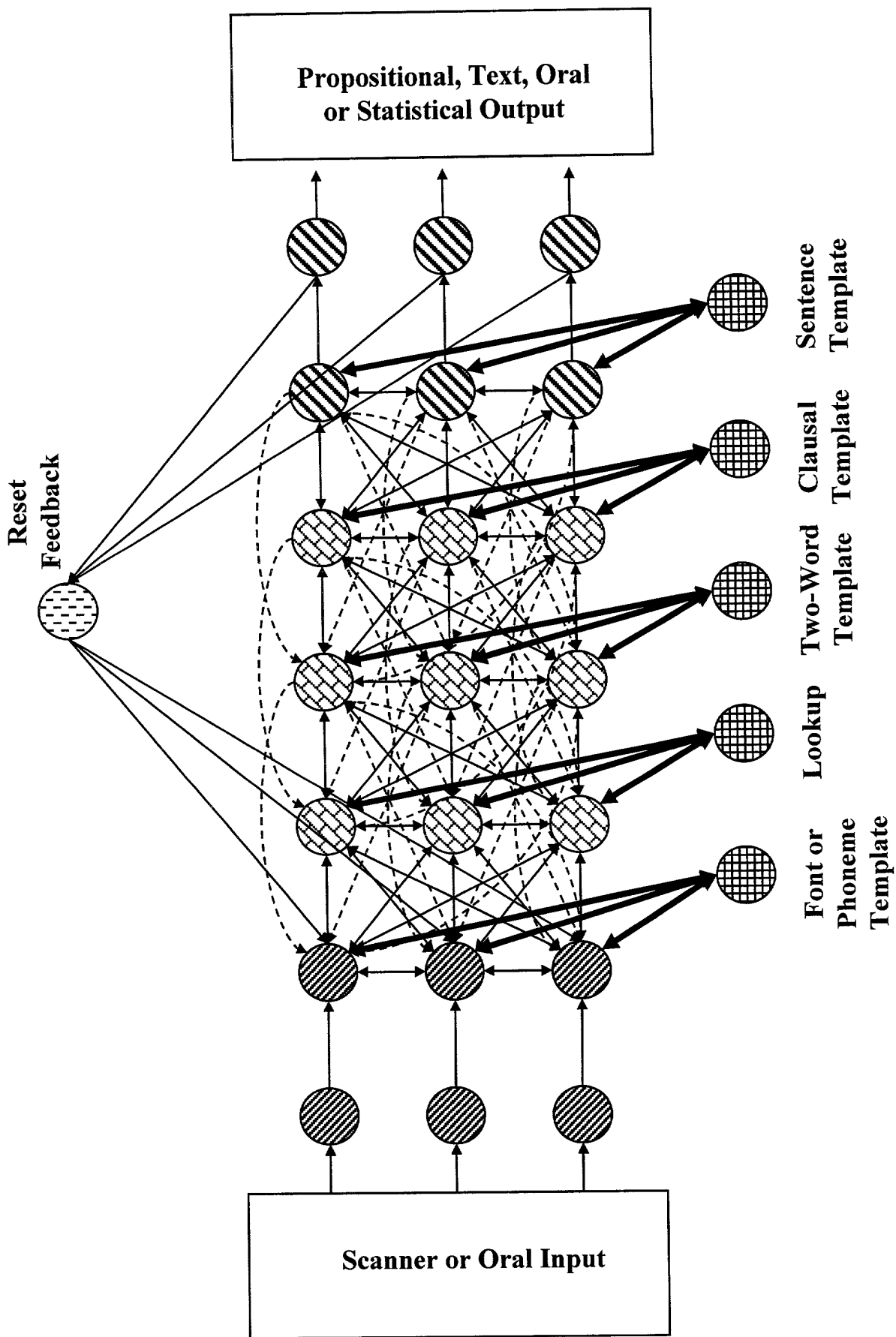


# Figure 24

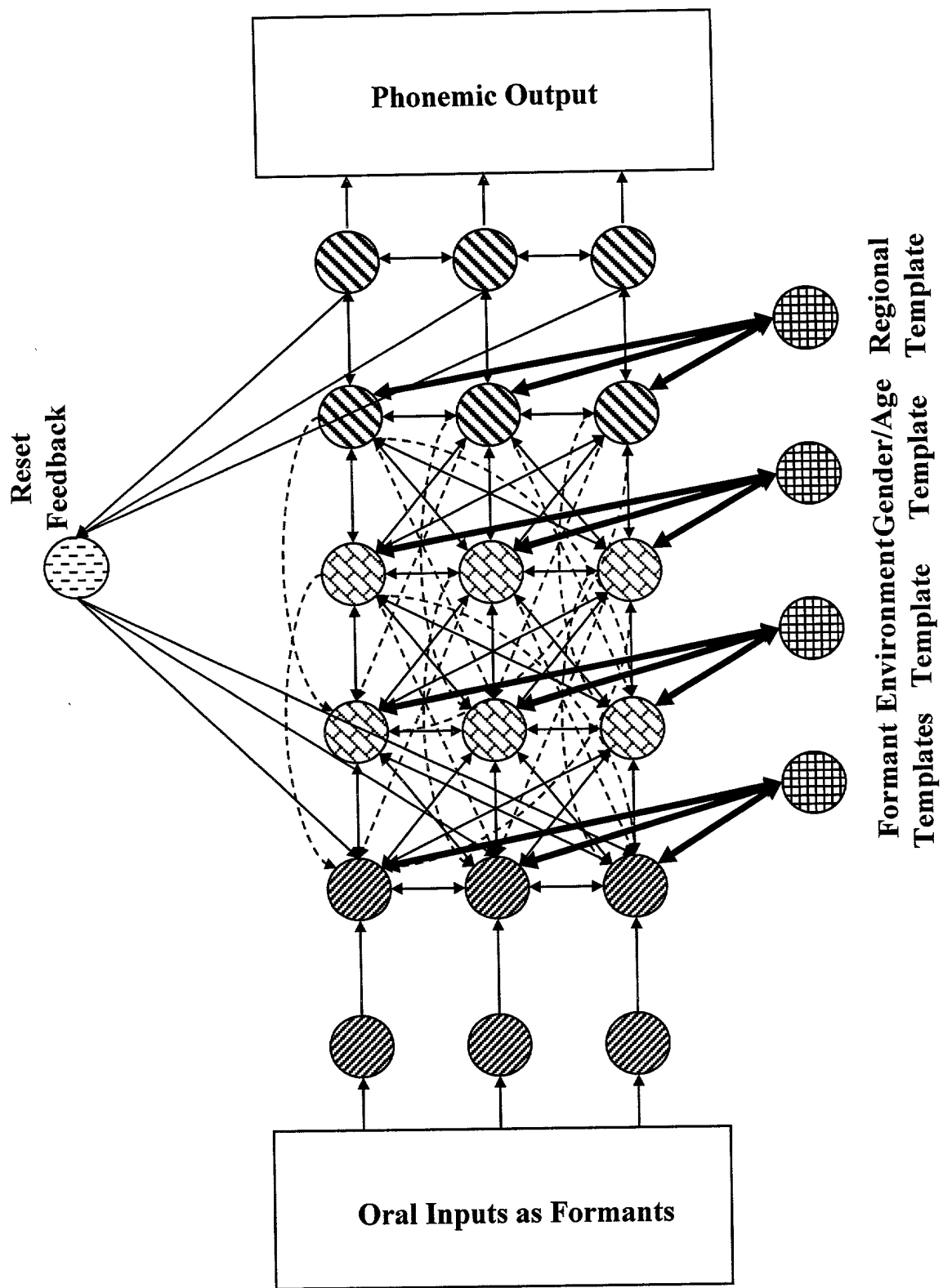




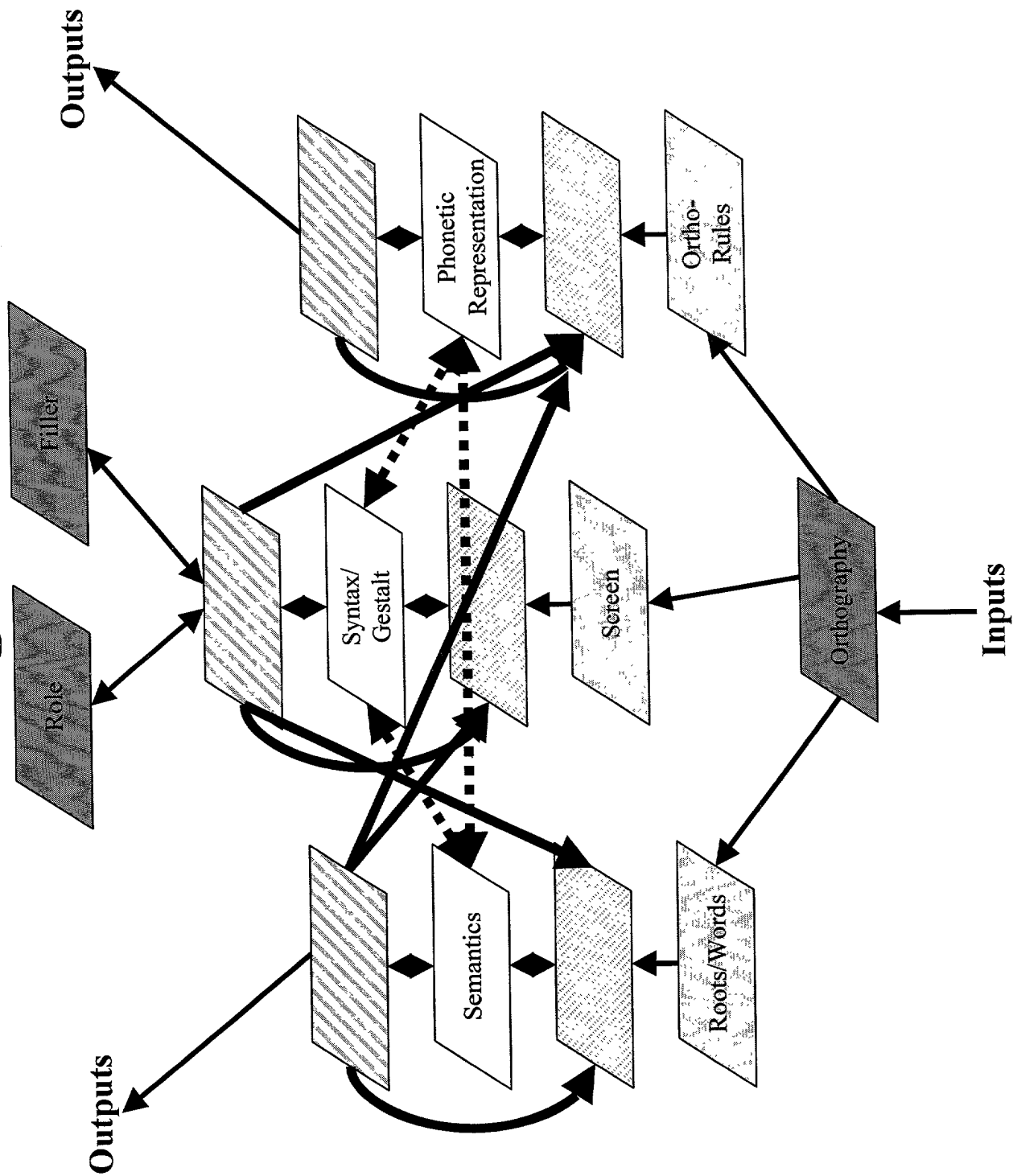
# Figure 25



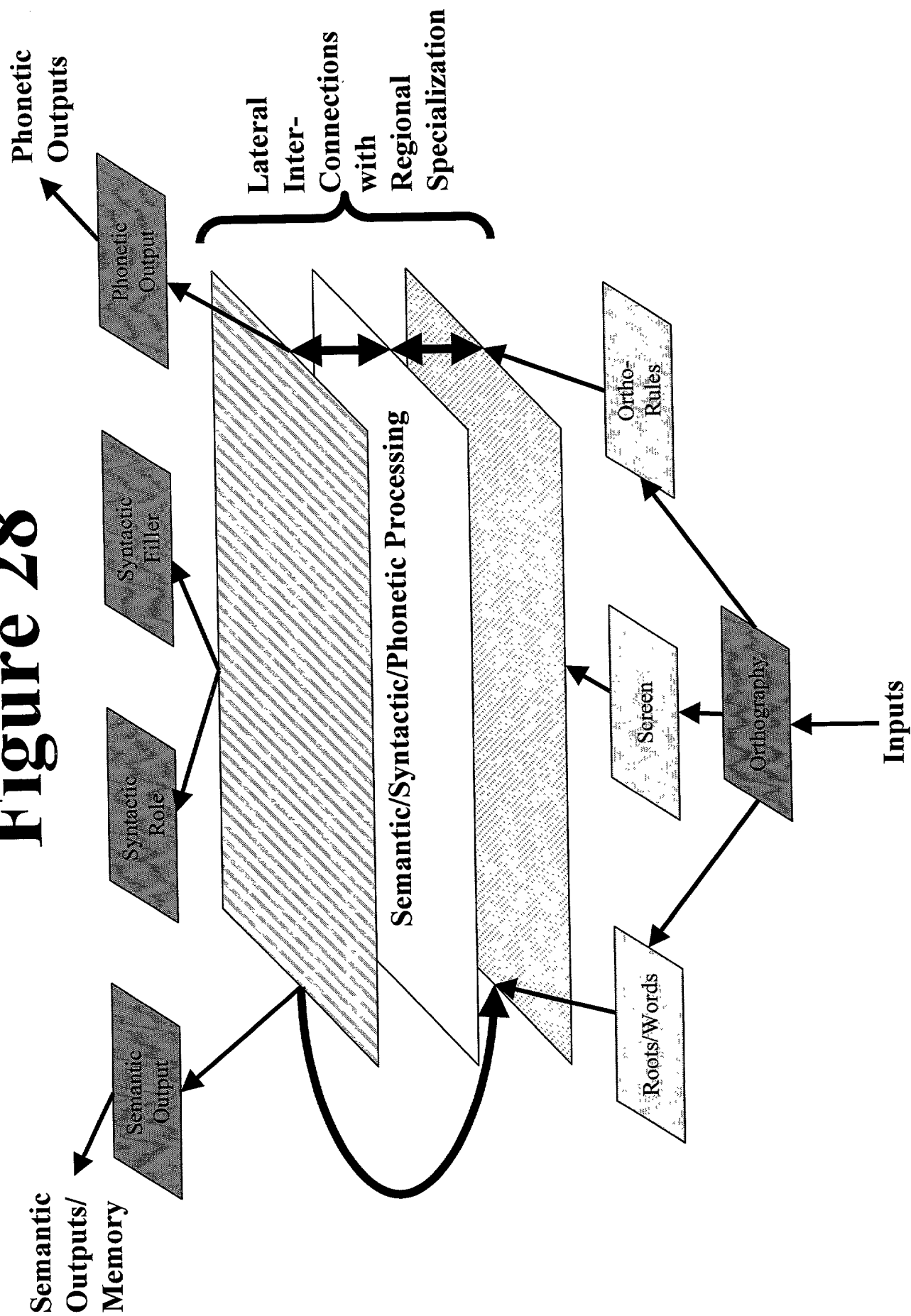
# Figure 26



# Figure 27



# Figure 28



**Figure 29**

